



PRODUCT/PROCESS CHANGE NOTIFICATION PCN 11255 – Additional information

STM32L4R/S products improvements

MDG - Microcontrollers Division (MCD)

How to order samples?

For all samples request linked to this PCN, please:

- place a **Non-standard** sample order (choose Sample Non Std Type from pull down menu)
- insert the PCN number “**PCN11255**” into the NPO Electronic Sheet/**Regional Sheet**
- request sample(s) through Notice tool, indicating a single Commercial Product for each request

Partial Ship: 01 Price Pol: 05 Status: 01 Canc: ☐

%: 0 Sample Type: Sample Non Std Type

Closing Type: Sample Std Type
Sample Non Std Type
Sample Non Std w Spl Tests

Lab Sheet:

SO | NPO Sample

Header

SO Nr: 8018502433 Customer: 99770200 01 ST-TOKYO SO Type: 30 Sample Order Cost Center: JT3129 SAMPLES /SALES J

PD Nr: Carrier Code: 0001 Price Policy: 05 Currency: 02 U.S. DOLLAR Req Name:

Notes: Status: 01 All items pending in Issuing Date: 25-JUN-2018 Ord Val: 0.0000 Sample Req Date: 25-Jun-2018

Sch I Nr	PD I. Nr	Finished Good	Comm Qty	Open Qty	Plant Open Qty	Reqd Qty	Unit Price	RD	CD	EDD	St
1.1.10	000001	STM32F429NIH6	30	30	30	30	0.0000	25-Jun-18	01-Mar-59	01-Mar-59	01

Final Cust: PO Item: 000001 Comm Prod: STM32F429NIH6 Qty: 30 RD: 25-Jun-18 Unit Price: 0.0000 Final Cust: 8800367006 SANSHIN/NPC

Cust Part Nr: Finished Good: Partial Ship: 01 Price Pol: 05 Status: 01 Canc: ☐

Notes: TAM K Pieces: 0 Our Share: 0 Sample Type: Sample Non Std Type

Project Name: Closing Date: Closing Type:

Regional Sheet: PCN 10595

Lab Sheet:



RERMCD1603 for PCN11255: Qualification Plan Die 470XXXW (Cut1.4)

Dec 13rd 2018

MDG MCD Quality & Reliability Department

PCN11255: Qualification Plan: Die 470XXXW (Cut 1.4)

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- Qualification plan for changes described in the PCN11255
- Reliability report: RERMCD1603

Trial	Test1	Method	Conditions	Sample x lot
				470 Rev W (Cut 1.4)
DIE	LU	0018695 JESD78	130°C	6 x 1
	ESD HBM	ANSI/ESDA/ JEDEC JS-001	Ta 25°C 2KV (Class 2)	3 x 1
	ESD CDM	ANSI/ESDA/ JEDEC JS-002	25°C 250V (class C3)	3 x 1
	HTOL	MIL-STD-883 Method 1005 JESD22-A108	125°C , 3.6V 168h	77 x 1

- Note: This plan will be executed with UFBGA169 (DSI option). CDM will be done only in this package and will validate that that there is no regression compared to previous cut

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